



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s) Allen et al.  
Docket No.: YOR920030175US1  
Serial No.: 10/661,041  
Filing Date: September 12, 2003  
Group: 2811  
Examiner: Cuong Q. Nguyen

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature: Susan Futner Date: November 1, 2005

Title: Techniques for Patterning Features in Semiconductor Devices

TRANSMITTAL LETTER

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Submitted herewith are the following documents relating to the above-identified patent application:

1. Response After Final Rejection;
2. Affidavit Under 37 C.F.R. §1.131; and
3. Petition for Extension of Time Under 37 C.F.R. 1.136(a)

There is no additional claim fee due in connection with the Response. In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **IBM Corporation Deposit Account No. 50-0510** as required to correct the error. A duplicate copy of this letter is enclosed.

Respectfully submitted,

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